Elect. Eng.(IE) FIFTH YEAR

Code	Semester	MODULE'S NAME	TYPE	Cre	The	Lab
				d	0	
13077	both	Electronic Equipment	Т	9	6	3
13089	both	Electronic Systems for Information	Т	12	7.5	4.5
		Processing				
13091	both	Telematic systems	Т	9	6	3
13084	2nd	Projects	Т	6	4.5	1.5
13085	1st	Automatic Regulation	OB	4.5	3	1.5
13072	1st	Electromagnetic Compatibility	OB	4.5	3	1.5
13088	2nd	Advanced Digital Systems	OB	4.5	3	1.5
13083	2nd	Final Degree Project	OB	15		
		Elective	OP	4.5		
		Free Choice	LO	4.5		
		TOTAL		73.5		

ELECTIVE SUBJECTS (can also be chosen as free choice subjects)

Code	Semester	Year	MODULE'S NAME	Cred	Theo	Lab
13075	1st	4th	Power Systems Design	6	3	3
12739	1st	4th	Electromagnetic waves	6	6	0
13078	2nd	4th	Digital Filtering	6	3	3
(*)	1st	5th	Power Industrial Systems	4.5	3	1.5
13092	1st	5th	Advanced Techniques for Information	4.5	3	1.5
			Processing			
13079	1st	5th	Biomedical Engineering	6	4.5	1.5
13076	2nd	5th	High Speed Digital Design	7.5	4.5	3
13087	2nd	5th	Instrumentation Systems	4.5	1.5	3
13051	2nd	5th	Robotics	6	4.5	1.5

(*) These subjects are not offered this academic year (2001/02)

B.Eng. in ELECTRONICS ENGINEERING



ACADEMIC YEAR 2001/02

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1.- Structure and organization of curriculum:

Degree: B.Eng in Electronic Engineering

{ Published at BOE num. 198 (18-08-00) } *Cycle*: Second.

Minimal Period of Lectures: 2 years, with semester and annual subjects.

1st Semester: last week of September- mid February. 2nd Ssemester: mid February until end of June

<u>Arrangement of the career in credits:</u> (1 credit = 10 hours)

Credits' arrangement		
CORE	(T)	81
COMPULSORY	(OB)	18
ELECTIVE	(OP)	13.5
FREE CHOICE	(LO)	14.5
FINAL CAREER PROJECT		15
Total credits:		142

<u>Note</u>: All students must attend to the whole core and compulsory subjects. They must select optional modules in order to fulfil the required 13.5 credits.

Free choice can be chosen out of any subject proposed in any degree at the Universidad de Valencia.

New Access Vacancies: 50

Direct Access from:

Engineering Degree in Telecommunications (first 3 years) Technical Engineering Degree in Telecommunications

- Orientation in electronic systems
- Orientation in telecommunication systems

Technical Industrial Engineering (orientation in industrial electronics)

Access Coursing Complementary credits:

- Physics degree (first 3 years)
- Computing engineering (first 3 years)
- Industrial engineering (first 3 years)
- Technical Industrial Engineering (orientation in electricity)
- Technical Eng. in Telecommunications (or: Image & sound, Telematics)
- Technical Computing Engineering (physical systems)

Location:

Facultad de FísicaCampus of Burjassot-PaternaUniversidad de ValenciaDepartamento de Ingeniería Electronica.Tel.: +34 96 3160450Fax.: +34 96 3160466http://www.uv.es/die/erasmus

Contact persons:

Degree coordinator: Enrique Maset. Email: Enrique.Maset@uv.es Socrates coordinator: Javier Calpe. Email: Javier.Calpe@uv.es

2.- Professional Skills:

The Electronics Engineer from the University of Valencia is trained to perform correctly in several branches of the electronics industry, e.g.:

- Electronics Instrumentation
- Electronic Systems (analogue and digital) for information processing
- Power Electronics
- Telematics
- Digital Signal Processing

This degree is conceived to respond the professional requirements of industry. Possible environments for our graduates are:

- Project managing in R+D departments.
- Development of products and technologies.
- Technology transfer.

These tasks can be developed in companies in the electronics, communication, computing, automotive, medical sector, and, in general, in any company that uses electronics as a relevant part of its production process. The scientific basis that back our engineers, enable them to carry out research (scientific and technological) in Universities and Public or Private Research Organisations. Anyway, these basis will ease their future re-cycling that should assure a perfect update in their future work in Electronics.

3.- Subject's Map

COMPLEMENTARY MODULES

Code	Semest	MODULE'S NAME	Туре	Credit	Theor	Lab
	er			S	у	
13095	both	Analysis of Linear Circuits and Systems	OB	9	7.5	1.5
13116	1st	Microelectronics	OB	6	3	3
13097	both	Digital Electronic Devices and Circuits	OB	10.5	6	4.5
13100	both	Analogue Electronics I	OB	12	7.5	4.5

Elect. Eng.(IE) FOURTH YEAR

Code	Semest	MODULE'S NAME	Туре	Credit	Theor	Lab
	er			s	у	
13080	both	Electronic Instrumentation	Т	12	6	6
13094	both	Signals Processing and Transmission	Т	9	6	3
13074	both	Electronic Circuits and Systems Design	Т	12	7.5	4.5
13073	1st	Electronics & Photonic Devices	Т	7.5	6	1.5
13093	2nd	Electronics & Photonic Devices	Т	4.5	3	1.5
		Technology				
13082	1st	Digital Signal Processing	OB	4.5	3	1.5
		Elective	OP	9		
		Free Choice	LO	10		
		TOTAL		69		