I.T.T.-S.E. THIRD YEAR

Code	Semester	MODULE'S NAME	Type	Credits	Theory	Lab
13124	Both	Electronic Control Systems	T	9	6	3
13111	Both	Introduction to Power Electronics	OB	9	6	3
13106	1st	Foundations on communication electronics	OB	6	4.5	1.5
13119	2nd	Projects	T	6	4.5	1.5
13118		Final Career Project	OB	15	0	15
		Elective	OP	10.5		
		Free Choice	LO	15		
		TOTAL		**70.5**		

OPTIONAL SUBJECTS (can also be chosen as free choice subjects)

CODE	MODULE'S NAME	Semester	Year	CREDITS	Theo	Lab
13112	Introduc. to Programmable Logic Controllers	1st	2nd	7.5	4.5	3
12510	Economy and industrial organisation	1st	2nd	6	6	0
13115	Mathematical methods for engineering	(*)	2nd	6	4.5	1.5
13123	Power supply electronic systems	2nd	2nd	7.5	4.5	3
13029	Programming tools	2nd	2nd	4.5	1.5	3
13129	Multimedia information processing	1st	3rd	4.5	3	1.5
13113	Introduction to telecommunication systems	1st	3rd	4.5	4.5	0
13121	Remote Sensing Systems	1st	3rd	4.5	3	1.5
13107	Assessment and Quality Control in Electronic	1st	3rd	6	6	0
13098	Optical communications	(*)	3rd	4.5	3	1.5
13126	Distributed industrial systems	(*)	3rd	6	3	3
13122	Electronic systems with microprocessors	2nd	3rd	6	3	3
13120	Data Transmission Networks	2nd	3rd	6	3	3
13096	Power electronic devices	2nd	3rd	6	4.5	1.5
13117	Microwaves and antennas	2nd	3rd	6	4.5	1.5
13109	Virtual instrumentation	2nd	3rd	6	3	3

(*) These subjects are not offered this academic year (2001/02)

TECHNICAL ENGINEERING IN TELECOMMUNICATIONS (ELECTRONIC SYSTEMS)

I.T.T.-S.E.

VNIVERSITAT DE VALÈNCIA

ACADEMIC YEAR 2001/02

Table of Contents

1.- *Structure and organization of curriculum:*

Degree: Technical Engineering Degree in Telecommunications
(Electronic Systems)
{Published at BOE num. 240 (6-10-2000)}

Cycle: First.

Minimal Period of Lectures: 3 years, with semester and annual subjects.
First semester: last week of September- mid February
Second semester: mid February until end of June

Arrangement of the degree in credits:

(1 credit = 10 hours)

Credits' arrangement		
CORE	(T)	105
COMPULSORY	(OB)	40.5
ELECTIVE	(OP)	25.5
FREE CHOICE	(LO)	21
FINAL CAREER PROJECT		15
Total:		**207**

Note: All students must attend to the whole core and compulsory subjects. They must select optional modules in order to fulfil the required 25.5 credits.
Free choice can be chosen out of any subject proposed in any degree at the Universidad de Valencia.

New Access Vacancies: **120** (Academic year 2001/02)

Gateways: Technical Engineering Degree in Telecommunications (Electronic Systems) brings direct access to second cycle in:

- B.Eng. Electronic Engineering.
- B.Eng. Control and Industrial Electronic Engineering.
- B.Eng. Telecommunications Engineering.

Location:
Facultad de Física Campus of Burjassot-Paterna Universidad de Valencia
Departamento de Ingeniería Electrónica. Tel.: +34 963160450 Fax: +34 963160466
http://www.uv.es/die/erasmus/

Contact persons:
Degree coordinator: Enrique Maset. Email: Enrique.Maset@uv.es
Socrates coordinator: Javier Calpe. Email: Javier.Calpe@uv.es

2.- *Professional Skills:*

The Technical Engineering Degree in Telecommunications (Electronic Systems) in the University of Valencia is a cluster of terms intended to train Technical Engineers, well-qualified for the electronic industry market and R&D activities, ready to analyse and solve technological challenges, ready to get involved and develop research projects.

The Technical Engineering Degree in Telecommunication (Electronic Systems) in the University of Valencia will be trained in Electronic Circuits Design, Digital Signal Processing, Telematics, Power Electronics, etc., and will be able to directly access the Electronic Engineer Degree (two years).

3.- *Subject's Map*

I.T.T. -S.E. FIRST YEAR

CODE	Semester	MODULE'S NAME	TYPE	Cred.	Theo	Lab
13104	both	Mathematical Analysis for Engineering	T	12	12	0
13100	both	Analogue electronics I	T	12	7.5	4.5
13095	both	Analysis of Circuits and Linear Systems	T	9	7.5	1.5
13097	both	Digital Electronic Devices and Circuits	T	10.5	6	4.5
12755	1st	Computing	OB	6	3	3
13103	2nd	Principles of Physic for Engineers	T	6	6	0
13102	2nd	Electronic Devices	T	6	3	3
		Elective	OP	6		
		TOTAL		**67.5**		

I.T.T. -S.E. SECOND YEAR

CODE	Semester	MODULE'S NAME	Type	Credits	Theory	Lab
13125	Both	Digital Electronic Systems	T	12	7.5	4.5
13110	Both	Instrumentation and Electronic Equipment	T	10.5	6	4.5
13127	Both	CAD	OB	6	0	6
13105	1st	Fundamentals of Computers Architecture	T	6	3	3
13114	1st	Introduction to Digital Signal Processing	OB	7.5	4.5	3
13116	2nd	Microelectronics	T	6	3	3
13101	2nd	Analogue electronics II	OB	6	4.5	1.5
		Elective	OP	9		
		Free Choice	LO	6		
		TOTAL		**69**		