



# SEMINAR ON MODEL-BASED DESIGN FOR POWER ELECTRONICS

Friday, April 28th

Escuela Técnica Superior de Ingeniería  
Room Aula 3.4



10:30 Deploying power electronic models to FPGAs

12:00 Coffee break

12:20 Model-Based Design for Power Electronics

13:50 Hands-on training with Simscape Electrical



Pablo Trujillo  
Power Electronics



Carlos Villegas  
Speedgoat



Pablo Romero Cumbreiras  
MathWorks

