On the Characterization of Interconnection Networks with Irregular Topology : A New Model of Communication Cost *

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Abstract

Networks of Workstations have become a cost-effective alternative to parallel computers. The characterization of these networks results quite difficult, since the traditional parameters used for regular topologies (node degree, diameter, average distance, etc.) do not provide information about the arrangement of the links. In this paper, we propose a new model of communication cost between network nodes. This model takes into account both the network topology and the routing algorithm, but it does not depend on the traffic pattern generated by the application running on the machine. The evaluation results show that our communication cost model is highly correlated with network performance. Since it provides a metric based on internode distance, our model can be used as the basis for both an efficient characterization of networks as well as an efficient mapping of processes to processors.

1 Introduction

Networks of Workstations have become a cost-effective alternative to parallel computers [2, 5]. In theses systems, workstations are connected via high-speed local area networks. Switch-based interconnects with irregular topologies [6, 10] provide the wiring flexibility required in these environments, also allowing the design of scalable systems with incremental expansion capabilities. Although interconnects may implement different switching techniques, wormhole switching is used in most cases, and several routing algorithms have been proposed for this switching technique [7, 9].

The performance of the applications running on a parallel machine may be communication-limited rather than computation-limited [1, 4]. In this case, the design of the network architecture is crucial. In massively parallel computers (MPPs), interconnection networks have been characterized by their topological properties, such as number of nodes, bisection width and diameter. However, the characterization of irregular networks cannot be based upon these parameters, since they do not provide information about the arrangement of the links. On other hand, the routing algorithm may seriously affect network performance by determining the traffic distribution in the network. Consider, for example, the routing scheme used in Autonet networks [10]. In this scheme some minimal paths are forbidden for routing, and the routing algorithm tends to overload links located near the root switch. As a result, the network may saturate prematurely. Therefore, the routing algorithm must be considered when characterizing irregular networks.

As a result of the above considerations, a new method based on grouping network nodes into clusters has been proposed for characterizing irregular networks [8]. This approach can easily identify the bottleneck links, helping the operating system to properly distribute the load between the correct group of workstations. However, in this approach the characterization of the network depends on the traffic pattern generated by the application running on the machine. Additionally, that traffic pattern may vary over time, thus limiting the applicability of this approach.

In this paper, another method to characterize irregular networks is proposed. The idea is to establish a model of communication cost between each node pair in the network that is independent of the traffic pattern, taking into account only the topology of the network and the routing algorithm. Therefore, this model can be used as the basis for an efficient mapping of processes to processors, since it provides a metric based on internode distance. We have studied the correlation of our model of communication costs with performance measures obtained by simulation. The results show that the proposed model is highly correlated with network performance, thus being a suitable metric for network characterization.

The paper is organized as follows. The proposed model and its properties are studied in Section 2. Section 3 analyzes the correlation of the proposed model with network performance obtained by simulation, validating the model as a static network performance indicator. Finally, Section 4 presents some concluding remarks and guidelines for future work.

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2 A New Model of Communication Cost

As mentioned in the previous section, our model of communication cost between two nodes only takes into account the topology of the network and the routing algorithm. We have assumed Up/Down routing [10] because it is used in several commercial networks. However, the model proposed in this paper can be applied to any routing algorithm. The idea is to define a metric that does not depend on the traffic pattern, allowing a characterization of the network based exclusively on the grouping of nodes in clusters with the lowest internode distances. The characterization of the network based on this metric will also allow us to efficiently map processes to processors, but mapping strategies are beyond the scope of this paper.

Our model proposes a simple metric, the equivalent distance between each pair of nodes (in what follows we will refer to a switching element as a node). This metric measures the cost of communicating between two nodes without explicitly considering traffic pattern. A table of equivalent distances can be obtained by computing the equivalent distance between each pair of nodes in the network. Unlike the metric proposed in [8], this metric does not consider different single link costs. We assume that link bandwidth is the same for all the links in the network. Therefore, we assign the unit cost for all single links in the network. The equivalent distance for a pair of nodes is computed taking into account all the shortest paths between them supplied by the routing algorithm. The name of the metric is derived from the analogy to the electrical equivalent resistance. Indeed, we use the same rules as for electrical circuits to compute the total communication cost between nodes, applying Kirchoff's laws.

In particular, the method used to construct the table of equivalent distances, computing the equivalent distance between each pair of nodes in the network, is the following one:

- 1. If there exists only one shortest path between a given pair of nodes then the communication cost between those nodes will be the sum of the costs of the links that form the path. That is, this case is similar to computing the equivalent resistance of an electrical circuit consisting of serially arranged resistors. Since we have assumed that all the links in the network have unit cost, the communication cost is equal to the number of links in the path.
- 2. If there exists more than one shortest path between a given pair of nodes then the communication cost between them is computed similarly to the electrical equivalent resistance between two points of an electrical circuit, replacing each link in a shortest path with a unit resistor and applying Kirchoff's laws. Note that we only consider the shortest paths provided by the routing algorithm. Those paths are not necessarily minimal (as is the case for Up/Down routing). Also,

the paths not supplied by the routing algorithm are not considered.

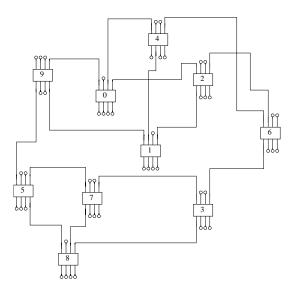


Figure 1: Autonet network with 10 switches

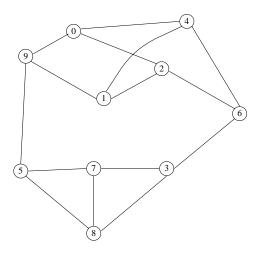


Figure 2: Graph G for the 10-node Autonet network

The application of this method will produce a table T_N with $N \times N$ equivalent distances, where N is the number of nodes in the network. In this table, the element T_{ij} represents the equivalent distance between node i and node j. As an example, consider the Autonet network shown in Figure 1. The Autonet routing algorithm is distributed, and implemented using table-lookup [10]. In order to fill the routing tables, the interconnection network can be modeled as a multigraph I = G(N,C), where N is the set of switches, and C is the set of bidirectional links between the switches. Figure 2 shows the graph for the network in Figure 1.

In order to fill the routing tables, a breadth-first spanning tree (BFS) is computed first, using a distributed algorithm. Based on this spanning tree, the link direction assignment is computed for graph *I*. The "up" end of a link is connected upper level node when the link connects nodes

located at different levels. When communicating nodes at the same tree level, the "up" end of a link is connected to the node with the lower label. The result of this assignment is that each cycle in the network has at least one link in the "up" direction and one link in the "down" direction. Figure 3 shows the link direction assignment for the graph in Figure 2. In this figure switches are arranged in such a way that all the switches at the same level in the spanning tree are at the same vertical position in the figure. The Up/Down routing scheme establishes that a legal route must traverse zero or more links in the "up" direction, followed by zero or more links in the "down" direction. A message cannot traverse a link along the "up" direction after having traversed a link in the "down" direction. Although such routing scheme is deadlock-free and allows some adaptivity, in some cases Up/Down routing is not able to supply any minimal path between two nodes.

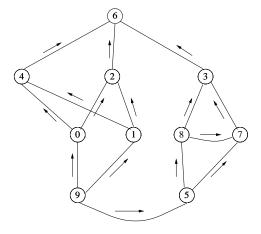


Figure 3: Link direction assignment for the graph in Figure 2

Ī	0-2-6-4-1	
	0-2-1	
	0-4-6-2-1	
	0-4-1	

Table 1: Possible paths for going from node 0 to node 1

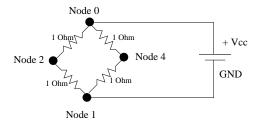


Figure 4: Model of equivalent resistance between node 0 and node 1

Let us consider the equivalent distance between nodes 0 and 1. Taking into account the link direction assignment

in Figure 3 and considering Up/Down routing, all possible paths for going from node 0 to node 1 are shown in Table 1, existing two shortest paths. Therefore, in order to compute the equivalent distance between nodes 0 and 1, the source and destination nodes must be considered as the V_{cc} and GND points of an electric circuit. Nodes 2 and 4 must be considered as two different intermediate points, and each link must be considered as a resistor with unit resistance, resulting in the circuit shown in Figure 4. Applying Kirchoff's laws to this circuit, an equivalent resistance of 1 Ohm is obtained. Thus, the equivalent distance from node 0 to node 1 is set to 1. The rest of equivalent distances in the table of distances are computed in the same way. The table of distances obtained for the network in Figure 1 is shown in Table 2.

Node	0	1	2	3	4	5	6	7	8	9
0	0	1	1	2	1	3	1	3	3	1
1	1	0	1	2	1	3	1	3	3	1
2	1	1	0	2	2	3	1	3	3	1
3	2	2	2	0	2	1	1	1	1	2
4	1	1	2	2	0	3	1	3	3	1
5	3	3	3	1	3	0	2	1	1	1
6	1	1	1	1	1	2	0	2	2	1.25
7	3	3	3	1	3	1	2	0	1	2
8	3	3	3	1	3	1	2	1	0	2
9	1	1	1	2	1	1	1.25	2	2	0

Table 2: Table of equivalent distances between nodes

An important property of the table is its symmetry. It is due to the behavior of the Up/Down routing algorithm. Effectively, there exist three cases when communicating two nodes: using only "up" paths (that is, paths that are exclusively formed by "up" channels), using only "down" paths (paths that only use "down" channels) and using "up-down" paths (paths that first use only "up" channels and then only "down" channels). When reversing communication direction, all "up" channels in a path become "down" channels, and therefore all "up" paths become "down" paths, and viceversa. However, "up-down" paths remain unchanged. As a result, all possible paths for communicating two nodes are the same for both communication directions, and therefore the table of distances is symmetrical. As a consequence, the table of equivalent distances determines a pseudo-metric space, with the following properties:

$$T_{ij} = 0 \iff i = j \tag{1}$$

$$T_{ij} = T_{ji} \tag{2}$$

However, the table of distances does not satisfy the triangular inequality, and thus it does not define a metric space. This limitation prevents the application of clustering methods that use Euclidean metrics for finding and characterizing groupings of nodes. However, the key issue for this table is its correlation with network performance. If it were well correlated, it would provide a simple method of measuring how far a node is from the rest of the network. In this case,

the characterization of the network based on this table could be done following the criterion of node proximity.

3 Model Validation

Each distance in the table of distances intends to measure the communication cost between each pair of nodes in the network. In order to ensure that these distances provide a good estimation of the communication cost, we must study the correlation between each distance in the table of distances and the average latency of the messages exchanged between the corresponding pair of nodes.

We have evaluated the performance of several irregular networks by simulation. The evaluation methodology used is based on the one proposed in [3]. The most important performance measures are latency and throughput. The message latency lasts since the message is injected in the network until the last flit is received at the destination node. Throughput is the maximum amount of information delivered per time unit (maximum traffic accepted by the network). Traffic is the flit reception rate. Latency is measured in clock cycles. Traffic is measured in flits per node per cycle.

3.1 Network Model and Message Generation

The network is composed of a set of interconnected switches. The network topology is irregular and has been generated randomly. However, for the sake of simplicity we imposed three restrictions. First, we assumed that there are exactly 4 nodes connected to each switch. Second, two neighboring switches are connected by a single bidirectional link. Finally, all the switches in the network have the same size. We assumed 8-port switches. Therefore, each switch has 4 ports available to connect to other switches. From these four ports, only three of them have been used at each switch in our simulation experiments. The remaining port is left open. We have evaluated networks with size ranging from 8 switches (32 nodes) to 24 switches (96 nodes). For some network sizes, several distinct topologies have been analyzed.

In order to study the correlation between the table of distances and the performance of the network, we have used a uniform distribution for message generation. Nevertheless, in order to ensure that the performance results do not depend on the traffic pattern, we have also used other nonuniform traffic patterns for message generation when evaluating some networks. In particular, we have also considered perfect-shuffle, bit reversal, and butterfly distributions. The message generation rate is constant and the same for all the nodes in the network. We have considered a fixed message size of 16 flits, and we have evaluated a wide range of traffic, ranging from low load to saturation.

3.2 Simulation and Correlation Results

Our simulator models the network at the flit level, and produces both global and per-node results. Global average latency shows the average latency of all messages transmitted through the network for a given traffic load. It provides an estimation of the degree of saturation of the network. On the other hand, the average latency for each source-destination pair in the network allowed us to study the correlation of the table of distances with network performance.

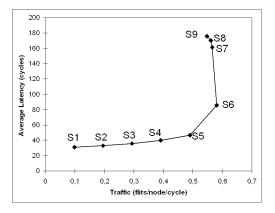


Figure 5: Global performance results for the 10-node network in Figure 1

Figure 5 shows performance evaluation results for the 10-switch network in Figure 1. This figure shows the global average message latency. Also, average message latencies for each pair of nodes were computed. However, they are not shown here due to space limitation.

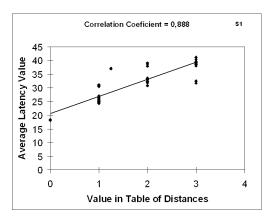


Figure 6: Least square linear adjustment for simulation point S1

In order to establish the correlation between the table of distances and these performance evaluation results, we first computed the least square linear adjustment for each point in Figure 5. The results for the first point S1 (traffic equal to 0.097 flits/node/cycle and global average latency equal to 30'93 cycles) are shown in Figure 6. In this figure,

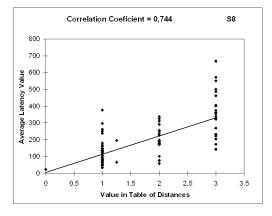


Figure 7: Least square linear adjustment for simulation point S8

the values in the table of distances are shown in the X-axis, while the specific latencies obtained by the simulator for each pair of nodes are shown in the Y-axis. The correlation index obtained in this case was 88.8%. We computed the same least square linear adjustment for all the points in the plot in Figure 5. Figure 7 shows the regression curve when the network is under deep saturation (point S8 in Figure 5, with traffic load equal to 0.56 flits/node/cycle and average latency equal to 170.17 cycles). The correlation index is 74.4% in this case. That is, there is a strong correlation between equivalent distances and the corresponding average latencies for all load conditions of the network. However, correlation decreases as the network approaches saturation.

Nevertheless, we have also studied the correlation between the equivalent distances and the average values of the average latencies supplied by our network simulator, weighted by the number of occurrences of the corresponding distance in the table of distances. Figure 8 shows the corresponding regression curve for simulation point S8. In this figure, there is a point for each different value in the table of distances. These values are represented in the X-axis. The number of occurrences for each value is shown on the right side of the figure. For example, point P2 represents value 1.25 in the table of distances and appears twice in the table of distances (see Table 2). Similarly, point P1 represents value 1 in the table of distances, and appears 42 times in Table 2. The weighted average values of average latencies show a very high correlation with the table of distances (99.2%).

In order to ensure that the topology does not affect correlation results, 6 different randomly generated 16-node topologies have been evaluated. We have computed the average value of the correlation coefficients both when the network is under low load, as well as when the network is deeply saturated. The average correlation coefficient for average latencies is 81.9%, with a standard deviation of 2%. When the network is deeply saturated, the average correlation coefficient falls to 46%, with a standard deviation of 12%. How-

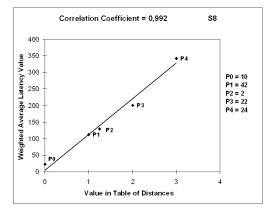


Figure 8: Least square linear adjustment for weighted average values of average latencies

ever, the average correlation coefficient for weighted average latencies reaches 89.9% with a standard deviation of 2% when the network is under low load. When the network is deeply saturated, the average value of this correlation coefficient decreases to 83.2%, with a standard deviation of 7%. Therefore, the computed standard deviations show that the correlation between the table of distances and network performance is not significantly affected by the network topology, particularly when the weighted average latencies are used as the performance measurement.

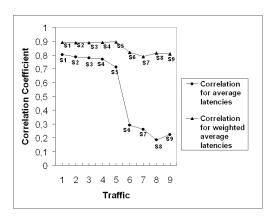


Figure 9: Correlation of average latency values and weighted average values of average latencies for a 24-switch network

Figure 9 shows correlation measurements for a 24-switch network. As in smaller networks, the correlation coefficient for latency values remains about 70-80% when the network is under low and medium load (points \$1 to \$5). However, when the network approaches saturation, the correlation coefficient falls down to values about 25-30%, and even reaching values below 20% when it is deeply saturated. On the other hand, the weighted average values of average latencies obtain a correlation coefficient of about 90% when the network is under low load and a correlation coefficient about

80% when the network reaches saturation, clearly showing that the correlation of this parameter is almost independent of traffic load.

Additionally, we have evaluated a 20-node network under the uniform traffic distribution as well as under three different nonuniform traffic distributions. The goal is to analyze whether the proposed approach (that does not take into account the traffic pattern when constructing the table of distances) is valid and accurate enough for any traffic distribution. In particular, we have evaluated the network under bit reversal, perfect shuffle and butterfly distributions. Figure 10 shows the correlation results for the 20-node network under the bit reversal traffic distribution. The results do not significantly differ from the correlation results for the same network under uniform traffic distribution. As when analyzing the effect of network topology on the correlation results, we have computed the average correlation coefficient for different traffic patterns both when the network is under low load as well as when the network is deeply saturated. The computed average correlation coefficient for average latencies is 96.4% with a standard deviation of 2% when the network is under low load, and decreases to 58.5% with a standard deviation of 13% when the network is deeply saturated. However, the average correlation coefficient for weighted average latencies is 99.1% with a standard deviation of 0.4% when the network is under low load, and only falls to 86.2% with a standard deviation of 1% when the network is deeply saturated. Therefore, the computed standard deviations allow us to conclude that the correlation of the table of distances with network performance does not significantly depend on the traffic pattern, particularly when the weighted average latencies are used as the network performance measurement.

4 Conclusions and Future Work

In this paper, a new model of communication cost is proposed in order to provide a single method for characterizing networks with irregular topology. This model introduces a new metric, the equivalent distance between each pair of nodes. By computing the equivalent distances between all the nodes in the network, the table of equivalent distances is formed. This table is symmetrical but does not define a metric space. Although this feature prevents the use of clustering algorithms based on Euclidean metrics, the most important feature of the table of distances is its correlation with network performance.

We have evaluated the correlation of the table of equivalent distances with network performance. The evaluation study was performed on randomly generated irregular networks with size ranging from 8 to 24 switches (32 to 96 nodes). The results show that the proposed metric is strongly correlated with the average latency obtained by simulations. However, for all the network sizes the degree of correlation decreases drastically when the network reaches saturation. Nevertheless, the proposed metric is highly correlated with

the weighted average values of average latencies obtained by simulation. Furthermore, the correlation of the table of distances with these weighted average values is not significantly affected when the network enters saturation. Additionally, these weighted average values are independent of both traffic pattern and network topology.

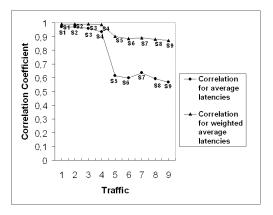


Figure 10: Correlations for a 20-switch network using bit reversal distribution as the traffic pattern.

As a result, we conclude that although the table of equivalent distances does not define a metric space, it can be used by clustering algorithms based on internode distances as the main communication cost index. Furthermore, the resulting clusters can be used as a basis for an efficient mapping of processes to processors. As for future work, we plan to develop efficient clustering algorithms based on the table of equivalent distances, as well as efficient process mapping algorithms.

References

- A. Agarwal, "Limits on interconnection network performance" *IEEE Transactions on Parallel and Distributed Systems*, vol. 2, no. 4, pp. 398–412, October 1991.
- [2] T. E. Anderson, T. E. Culler and D. A. Patterson, "A Case for NOW (Networks of Workstations)," *IEEE Micro*, pp. 54–64, February 1995.
- [3] J. Duato, "A new theory of deadlock-free adaptive routing in wormhole networks," *IEEE Trans. Parallel and Distributed Systems*, vol. 4, no. 12, pp. 1320–1331, December 1993.
- [4] J. Duato, S. Yalamanchili, L. Ni, Interconnection Networks: An Engineering Approach, IEEE Computer Society Press, 1997.
- [5] R. Felderman et al., "Atomic: A High Speed Local Communication Architecture," J. High Speed Networks, vol. 3, no. 1, pp.1-29, 1994
- [6] R. Horst, "TNet: A Reliable System Area Network," *IEEE Micro*, vol. 15, no. 1, pp. 37–45, February 1995.
- [7] R. Horst, "ServerNet deadlock avoidance and fractahedral topologies," in *Proceedings of the International Parallel Processing Symposium*, pp. 274–280, April 1996.
- [8] T.M. Pinkston and W. H. Ho, "A Clustering Approach in Characterizing Interconnection Networks", in *Proceedings of Fifth International Conference on High Performance Computing*, pp. 277-284, December 1998.
- [9] W. Qiao and L. M. Ni, "Adaptive routing in irregular networks using cut-through switches," *Proceedings of the 1996 International Con*ference on Parallel Processing, August 1996.
- [10] M. D. Schroeder et al., "Autonet: A high-speed, self-configuring local area network using point-to-point links," Technical Report SRC research report 59, DEC, April 1990.